

What is claimed is:

1. A semiconductor device which comprises external interface terminals and processing circuits, and which is fed with an operating power source when detachably set in a host equipment, wherein:

said external interface terminals include power source feeding terminals, an extraction detecting terminal, and other terminals;

said power source feeding terminals are long enough to keep touching corresponding terminals of the host equipment for, at least, a predetermined time period since separation of said extraction detecting terminal from a corresponding terminal of the host equipment; and

said power source feeding terminals are formed to be longer in an extraction direction than said extraction detecting terminal.

2. A semiconductor device according to claim 1, wherein said power source feeding terminals are made longer than said extraction detecting terminal, also on a side opposite to the extraction direction, and a length which said power source feeding terminals protrude on the opposite side to the extraction direction, beyond said extraction detecting terminal, is smaller than a length which they protrude in the extraction direction.

3. A semiconductor device according to claim 1, wherein

when said semiconductor device has been set in the host equipment, each of said power source feeding terminals touches the corresponding terminal of the host equipment at two points along the extraction direction.

4. A semiconductor device which comprises external interface terminals and processing circuits, and which is fed with an operating power source when detachably set in a host equipment, wherein:

said external interface terminals include power source feeding terminals, an extraction detecting terminal, and other terminals; and

said power source feeding terminals are long enough to touch corresponding terminals of the host equipment for, at least, 1.0 millisecond since separation of said extraction detecting terminal from a corresponding terminal of the host equipment, with respect to an extraction speed of 2.5 meters/second.

5. A semiconductor device according to claim 4, wherein said power source feeding terminals are formed to be longer in an extraction direction than said extraction detecting terminal.

6. A semiconductor device according to claim 5, wherein said power source feeding terminals are made longer than said extraction detecting terminal, also on a side opposite to the extraction direction, and a length which said power source

feeding terminals protrude on the opposite side to the extraction direction, beyond said extraction detecting terminal, is smaller than a length which they protrude in the extraction direction.

7. A semiconductor device according to claim 4, wherein when said semiconductor device has been set in the host equipment, each of said power source feeding terminals touches the corresponding terminal of the host equipment at two points along an extraction direction.

8. A semiconductor device which comprises external interface terminals and processing circuits, and which is fed with an operating power source when detachably set in a host equipment, wherein:

said external interface terminals are arranged in two rows in a direction crossing an extraction direction, and they include power source feeding terminals, an extraction detecting terminal, and other terminals; and

said power source feeding terminals are long so as to extend from the first row over to the second row.